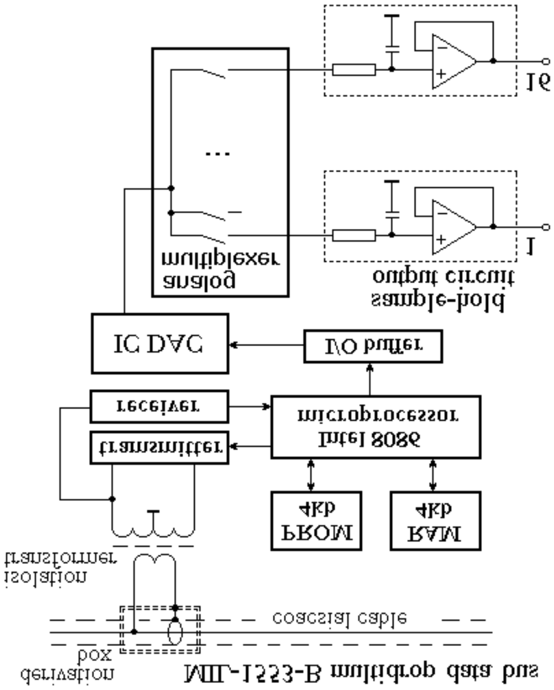
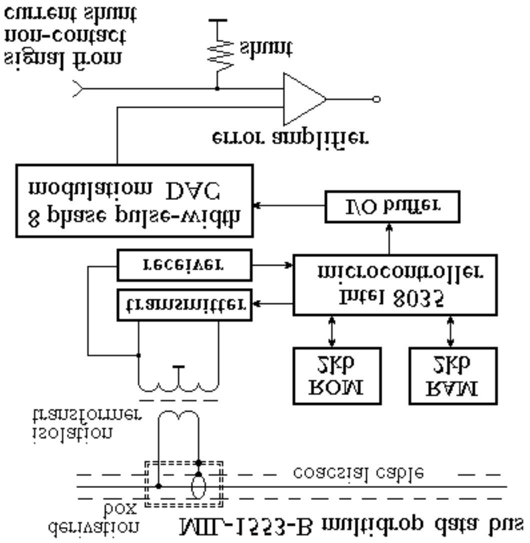
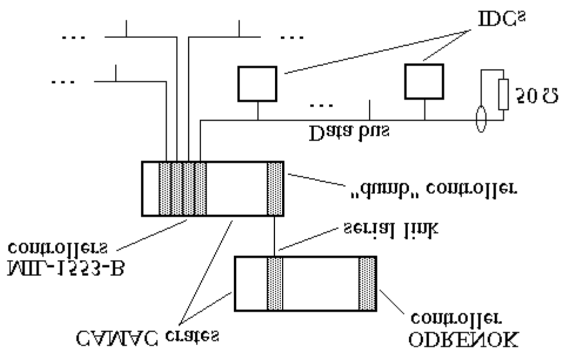
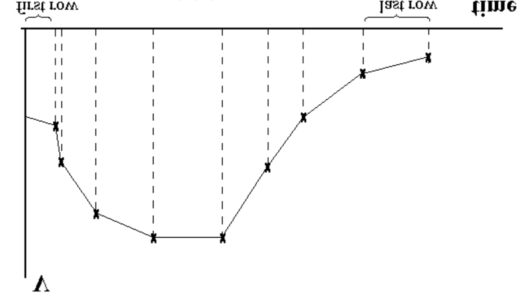
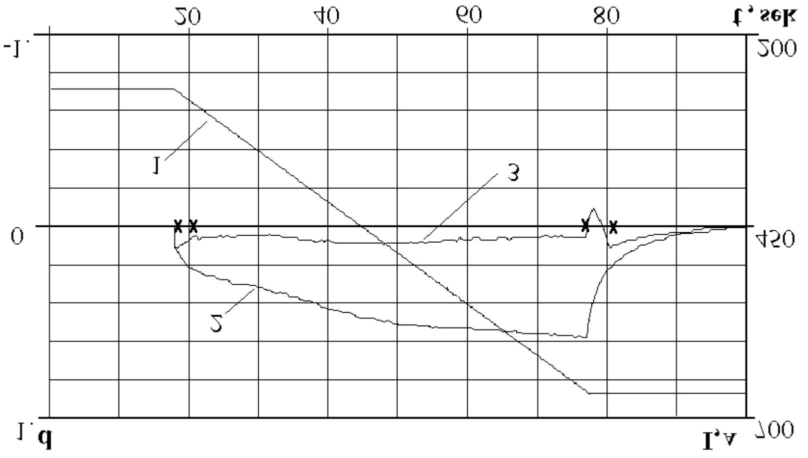

16
1
analog multiplexer
sample-hold output circuit
IC DAC
I/O buffer
receiver
transmitter
microprocessor Intel 8086
4kb PROM
4kb RAM
isolation transformer
coaxial cable
derivation box
MIL-1553-B multidrop data bus


signal from non-contact current shunt
shunt
error amplifier
8 phase pulse-width modulation DAC
I/O buffer
receiver
transmitter
microcontroller Intel 8035
2kb ROM
2kb RAM
isolation transformer
coaxial cable
derivation box
MIL-1553-B multidrop data bus


IDCs
50 Ω
Data bus
"dumb" controller
serial link
MIL-1553-B controllers
CAMAC crates
ODRENOK controller


first row
time setting
. . .
last row
time setting
time
V



1 - linear rise of the current

2 - delay of the dynamic magnetic field (**H**) relatively to static (**H**st)

3 - correction of the delay by addition of four intermediate points between initial and final points

x - correction points